

Substitute for form 1449/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

**Complete if Known**

<b>Application Number</b>	10/600,563
<b>Filing Date</b>	6/19/2003
<b>First Named Inventor</b>	Gunn III, et al.
<b>Art Unit</b>	2826
<b>Examiner Name</b>	Dickey, Thomas L.
<b>Attorney Docket Number</b>	LUX-P003

Sheet 1 of 1

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	1	ROBERT F. PIERRET, "Semiconductor Device Fundamentals," Prentice Hall, 2nd Revised Edition, 1995	
	2	ANDREW MARSHAL and SREEDHAR NATARAJAN, "SOI Design: Analog, Memory and Digital Techniques," Springer, 2001	
	3	Sleight, J. W., et al. "A High Performance 0.13 $\mu$ m SOI CMOS Technology with a 70 nm Silicon Film and with a Second Generation Low-k Cu BEOL," Electron Device Meeting, 2001	
		IEDM Technical Digest, page 11.3.1-11.3 (2001)	

Examiner  
SignatureDate  
Considered

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.

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